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THEM THE CAC G 1

9016 (May)

COMPUTER SCIENCE (General)

Course ; 601

Computer Organization and Architecture)

Full Marks ; 48 Pass Marks ; 19

Time : 2 hours

The figures in the margin indicate full marks for the questions

- 1. Choose and write the correct option: 1×6=6
 - (a) The decimal equivalent of the hexadecimal number (13.28)₁₆ is

elqifinM (fi)

taid bin

AND NAME

(i) (396)₁₀

(ii) (392)₁₀

(iii) (400)₁₀

(lv) (393)₁₀

P15-500/644

(Turn Over)

C SEM THE CEC G 1 ___ directs data from input to selected output line. (i) Demultiplexer (ii) Multiplexer COMPUTER SCIENCE (iii) General | (iv) Register Course : 601 The ALU makes use of -- to store the intermediate results. (i) accumulators M IIII Pass Marto (ii) registers (iii) heap The figures in the margin indic (iv) stack marksup and rol - bus structure is usually used to (d) connect I/O devices. Ismioeb edit decimal number (13-28; (i) Single 01(000) (1) (ii) Multiple (1) (392)10 (iii) Star offOOA 6th (iv) RAM (m) (393) o (Continued) P15-500/644

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(c) The small extremely fast RAMs are
       2808 To margain sooid and ward 2808
                     Explain the working of Au
                              toangorgeroim
           (iii) accumulators in the quantity a
           Explain, in brief, direct nashats (vi)
      () Which of the following addressing
       modes is not possible in 8085?
           (i) Indexed addressing
      (ii) Indirect addressing
           (iii) Direct addressing
      olds
           (iv) Indirect register address
  2. Answer the following:
                                            2×6=12
          Draw a 2 to 1 line multiplexer circuit.
          What is instruction format?
         Which register in CPU is responsible for
     (c)
         sequencing the control of execution?
         Define cache hit, cache hit time and
         cache miss penalty.
         What is virtual memory?
         What is interrupt?
P15-500/644
              6 SEM
                                      (Turn Over)
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(f)

3. Anso	Familian the working of AU to A7 pins	13-30 H
	8085. Draw the block diagram of 808 microprocessor.	5 3+3-5
(b) (c)	Explain, in detail, CPU register. Explain, in brief, direct memory access	6
	Explain the importance of secondar memory in implementing virtua	9
Call	memory.	- 6
(8)	commonly used in processors.	6
(f)	Explain with diagram and truth tab J-K flip-flop.	le 6
11-8-12	generative following *** Dense a 2 to 1 inse multiplexes our	
	Strategic American Strategic Action of the Company	
	Which register in CFV is responsible and approximation of papersing.	
	Synomer laurets at ted#	
	Street was a surface	